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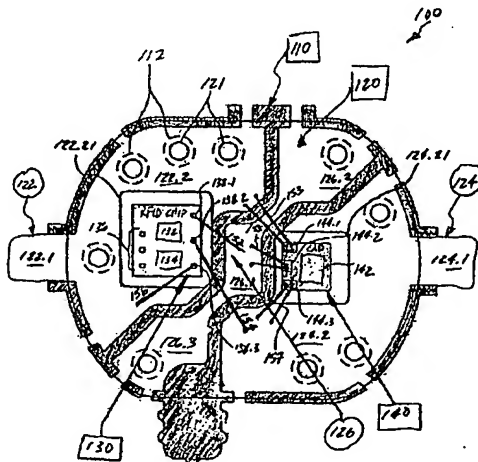
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(54) Title: STRUCTURES AND ASSEMBLY METHODS FOR RADIO-FREQUENCY-IDENTIFICATION MODULES



(57) Abstract: Radio-frequency identification (RFID) devices are used in a variety of applications to facilitate the identification and tracking of people, objects, and animals. One problem with RFID devices or tags concerns manufacturing cost. Specifically, some tag designs use an integrated-circuit chip requiring three external connections, instead of two as do many other designs. Accordingly, the present inventor devised a unique RFID module (100) which uses a three-terminal integrated circuit (130) and two capacitors, but only requires two external leads. One exemplary embodiment of the module includes two external terminals (122-124) for connection to an antenna coil (220) and an internal terminal for connection to a center-tap of a dual (center-tapped) capacitor (142). Other aspects of the invention include subcomponents of the module and methods of tag assembly using the module.

Structures and Assembly Methods for Radio-Frequency-Identification Modules

5 Related Application

This application is a continuation of U.S. Provisional Application 60/223071, which was filed on August 4, 2000. This application is incorporated herein by reference.

Technical Field

10 The present invention concerns radio-frequency identification devices,
particularly related assembly methods and structures for modules in such
devices.

Background

Radio-frequency identification (RFID) devices are used in a variety of applications to facilitate the identification and tracking of people, objects, and animals. Each device, also known as a tag or smart card, can be attached to a person, object, or animal. The devices typically include an antenna coil, radio circuitry, and a memory, enabling them not only to receive radio-frequency signals from a reader or interrogation device, but also to send back signals representing the data in their memories. The data, generally a unique identification number, can then be used with a computerized database to associate the tagged person, object, or animal with other data, such as price, shipping, inventory, or owner information.

One problem with RFID devices or tags concerns manufacturing cost. In particular, the inventor recognized that some tag designs use a module requiring three external connections, instead of two as do many other designs. For example, one such design uses a module that has three external terminals A, B, and C, and requires connection of an antenna coil between terminals A and C and connection of one external capacitor between terminals A and B and another external capacitor between terminals B and C. Although the two capacitors in the design provide an increased communication range, the additional complexity

of connecting to the extra terminal restricted commercial application for the design.

Accordingly, the inventor identified a need for a better way of manufacturing tags that require three external terminals and two capacitors.

5

Summary

To address these and other needs, the present inventor devised a unique radio-frequency-identification (RFID) module which uses two capacitors, but only requires two external connections. In one exemplary embodiment, the module includes two external terminals for connection to an antenna coil and an internal terminal for connection to a tap of a dual (center-tapped) capacitor. Other aspects of the invention include components of the module and methods of assembling tags using the module.

Brief Description of the Drawings

Figure 1 is a plan view of an exemplary RFID module 100 incorporating teachings of the present invention.

Figure 2 is a conceptual view of an exemplary RFID device 200 incorporating RFID module 100 of Figure 1.

Figure 3 is a flow chart illustrating an exemplary assembly method based on the RFID module 100 and RFID device 200.

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Detailed Description of Exemplary Embodiments

The following detailed description, which references and incorporates the above-identified figures, describes and illustrates one or more specific embodiments of the invention. These embodiments, offered not to limit but only to exemplify and teach, are shown and described in sufficient detail to enable those skilled in the art to implement or practice the invention. Thus, where appropriate to avoid obscuring the invention, the description may omit certain information known to those of skill in the art.

Figure 1 shows an exemplary two-terminal RFID module 100 incorporating teachings of the present invention. Module 100 includes insulative structure 110, lead frame 120, integrated-circuit chips (or dies) 130 and 140, and conductor set 150.

Insulative structure 110 includes a number of rivet-like posts 112, which project through holes or openings 121 in lead frame 120, securing the relative position and electrical isolation of various portions of lead frame 120. In the exemplary embodiment, insulative structure 110, which is generally rectangular, is formed of an insulative mold material, such as a semiconductor transfer-molding compound. One commercially available compound is MG46F-AM compound from Dexter Hysol.

In addition to openings 121, lead frame 120 includes contact structures 122 and 124 and an intermediate contact structure 126. Contact structure 122 includes an extended portion 122.1 and a body portion 122.2. Extended portion 122.1, which has an exemplary thickness of 0.006 inches, extends beyond insulative structure 110 by about 0.057 inches. Body portion 122.2 includes a rectangular recessed region 122.21. Recessed region 122.21, which has an exemplary thickness that is 50-80% of the thickness of the remainder of the body portion, is formed, for example, by etching or stamping.

Similarly, contact structure 124 which lies opposite contact structure 122, includes an extended portion 124.1 and a body portion 124.2. Extended portion 124.1, which has an exemplary thickness of 0.006 inches, extends beyond insulative structure 110 by about 0.057 inches. Body portion 124.2 includes a rectangular recessed region 124.21. Recessed region 124.21 has a thickness in the range of 50-80% of its surrounding regions.

Inclusion of the recessed regions in body portions 122.2 and 124.2 reduces the overall height of module 100. However, other embodiments can reduce module height by removing all or part of material within the recessed regions, defining holes or slots.

Lying between contact structures 122 and 124 is intermediate contact structure 126. Intermediate contact structure 126 includes a central portion 126.1 and end portions 126.2 and 126.3. Central portion 126.1 is not only centered between end portions 126.2 and 126.3, but also approximately centered within lead frame 120.

The exemplary embodiment forms lead frame 100 from conductive material, such as OLIN 194 copper using conventional techniques, and then

silver-plates the lead frame to enhance its electrical conductivity. Other embodiments, however, plate the lead frame with other conductive materials, such as gold, or omit plating altogether. Some embodiments spot-plate highly-conductive runs on the contact structures to reduce the amount of plated material, whereas others form the entire lead frame from gold or silver. Still other embodiments form the conductive structure through deposition of material on 2 substrates. The invention is not limited to any particular material composition or method of forming lead frames.

In addition to insulative structure 110 and lead frame 120, RFID module 100 includes application-specific integrated-circuit (ASIC) chips 130 and 140, and conductor set 150. Integrated-circuit chip (or module) 130, which is attached to rectangular recess region 122.21 using a conventional chip-on-board technique, includes wireless-communications circuitry 132 and a memory circuit 134 to implement a passive RFID read-only tagging function. In the exemplary embodiment, the memory circuit is pre-programmed with data, such as a unique identification number; however, in other embodiments, for example, those that use read-write tagging circuitry, it need not be pre-programmed. Chip 130 also includes three external test terminals 136 for testing the RFID circuitry, and three external-connection terminals (or contact pads) 138.1, 138.2, and 138.3 for connection to external components, such as integrated-circuit chip 140.

Although the invention is not limited to any particular form of integrated-circuit chip 130, the exemplary embodiments uses the 13.56 MHz MCRF355 RFID chip from Microchip Technology, Incorporated of Chandler, Arizona. In this case, external-connection terminals 138.1, 138.2, and 138.3 correspond respectively to terminals A, B, and Vss.

Integrated-circuit module 140, which is attached to rectangular recess region 124.21 using a conventional chip-on-board or epoxy-globbing technique, includes an integrated dual or center-tapped capacitor 142 and has external-connection terminals (or contact pads) 144.1-144.3. Capacitor 142 includes a pair of end contacts and a center tap (not shown) which are coupled to terminals 144.1-144.3.

In the exemplary embodiment, each half of center-tapped capacitor 1142 has a nominal capacitance of 68 picofarads, with a tolerance of $\pm 3\%$. However, in other embodiments, the capacitor is not center tapped, meaning that each “half” has a different nominal capacitance. Some other embodiments use two
5 distinct capacitors, with each having one of its terminals (or nodes) connected to a common one of the external-connection terminals. Other embodiments may provide two pairs of external-connection terminals, with one pair coupled to one capacitor and the other pair coupled to the other capacitor.

Conductor set 150 includes wire-bond connections 152, 154, and 156 and
10 double-wire-bond connections 153, 155, and 157, which electrically connect integrated-circuit chips 130 and 140 to each other through lead frame 120.

Specifically, wire-bond connection 152 electrically connects intermediate contact structure 126 to terminal 138.1; wire-bond connection 154 electrically connects contact structure 124 to terminal 138.2; and wire-bond connection 156
15 connects contact structure 122 to terminal 138.3. Double-wire-bond connection 153 connects terminal 144.1 to contact structure 122; double-wire-bond connection 155 connects terminal 144.2 to intermediate contact structure 126; and double-wire-bond connection 157 connects terminal 144.3 to contact structure 124.

20 The exemplary embodiment forms these connections using a conventional gold-ball bonder. However, other embodiments use an aluminum-edge bonder. Additionally, the exemplary embodiment uses double-wire bonding for connections 153, 155, and 157 to facilitate automatic testing of the connections. Double-wire bonding can also be used for connections 152, 154,
25 and 156. The invention is not limited to any particular connection structure or technique.

These connections not only couple each of the three terminals of center-tapped capacitor 142 in chip 140 to one of the three external-connection terminals of chip 130, but also couple chip 140 to contact structures 122 and
30 124. As Figure 2 shows, this exemplary embodiment enables one to install a three-terminal RFID chip, such as the MCRF355 RFID chip from Microchip

Technology, Incorporated, in a conventional antenna substrate assembly using only two connections.

More particularly, Figure 2 shows an unlaminated RFID card (or tag) assembly 200 incorporating RFID module 100. In addition to module 100,
5 assembly 200 includes a conventional card substrate 210 and an antenna coil 220. Card substrate 210 includes a cutout 212 which has opposing notch 212.1 and 212.2 that exposes corresponding conductive portions 222 and 224 of antenna coil 220. Conductive portions 222 and 224 are coupled to extended portions 122.1 and 124.1 of extended contact structures 122 and 124 using
10 conventional techniques.

Figure 3 shows a flow chart 300 which illustrates an exemplary fabrication or assembly method based on module 100 and assembly 200 in Figures 1 and 2. The flow chart includes process blocks 310-350. The present invention is not limited to the order of the blocs in the Flow Chart 300.

15 The exemplary method begins at block 310 with provision of a lead frame in accord with lead frame 120 in Figure 1. Execution then proceeds to block 320, which entails attaching chips 130 and 140 to the lead frame. Block 330 entails completing the electrical connections of the chips to the three contact structures of the lead frame, and block 340 entails encapsulating the resultant
20 lead frame and chip assembly in an insulative material to yield module 100 as shown in Figure 1. The exemplary method concludes at block 350 with attachment or connection of two of the contact structures, for example, 122 and 124, of module 100 to an antenna coil on a card substrate, as shown in Figure 2.

Conclusion

25 In furtherance of the art, the inventors have presented an exemplary RFID module which uses a three-terminal integrated circuit and two capacitors, but only requires two external leads or connections. One exemplary embodiment of the module includes two external terminals for connection to an antenna coil and an internal terminal for connection to a center-tap of a center-tapped capacitor. Other aspects of the invention include the structure of various
30 components of the module and methods of tag assembly using the module.

The embodiments described above are intended only to illustrate and teach one or more ways of practicing or implementing the present invention, not to restrict its breadth or scope. The actual scope of the invention, which embraces all ways of practicing or implementing the teachings of the invention,
5 is defined only by the following claims and their equivalents.

What is claimed is:

1. A method of assembling a radio-frequency identification (RFID) module,
the method comprising:
 - 5 providing a lead frame having first, second, and third contact regions and
first and second recessed regions;
attaching an RFID chip to the first recessed region of the lead frame, with
the RFID chip having first, second, and third contact regions;
attaching a separate capacitor chip to the second recessed region of the
10 lead frame, with the separate capacitor chip including an
integrated silicon capacitor having first and second end contacts
and a tap;
electrically coupling the first end contact of the capacitor to the first
contact region;
15 electrically coupling the second end contact of the capacitor to the third
contact region;
electrically coupling the tap of the capacitor to the second contact
region;
electrically coupling the first contact region of the RFID chip to the
20 second contact region of the lead frame;
electrically coupling the second contact region of the RFID chip to the
first contact region of the lead frame;
electrically coupling the third contact region of the RFID chip to the third
contact region of the lead frame;
25 encapsulating at least partially the lead frame, the RFID chip, and the
capacitor chip in an insulative material.
2. The method of claim 1, wherein the second contact region of the lead
frame is between the first and third contact regions of the lead frame.
- 30 3. The method of claim 1, wherein the second contact region of the lead
frame is between the first and second recessed regions.

4. The method of claim 1, wherein the lead frame comprises silver-plated copper metal.
5. The method of claim 1, wherein electrically coupling the first end contact of the capacitor to the first contact region, comprises forming a wire-bond connection or forming an aluminum-edge bond.
6. The method of claim 1, wherein electrically coupling the first end contact of the capacitor to the first contact region, comprises forming a double-wire-bond connection.
7. The method of claim 1, wherein encapsulating at least partially the lead frame, the RFID chip, and the capacitor chip in an insulative material comprises: transfer molding using semiconductor transfer molding compound; or using an epoxy globing technique.
8. A method of making a radio-frequency-identification assembly, the method comprising:
providing an RFID module assembled according to claims 1, 2, 3, 4, or 5;
providing a substrate having an antenna coil and a pair of connection regions electrically coupled to the antenna coil; and
electrically coupling the first and third contact regions of the lead frame to the pair of connection regions.
9. A lead frame for use in making a radio-frequency-identification module, the lead frame including:
first and second recessed regions for receiving first and second chips; and
at least three contact pads, with at least two of the pads for connection to components external to the module.

10. A lead frame for use in making a radio-frequency-identification module,
the lead frame including:
a first conductive structure having a first body portion and a first
extended portion, with the first extended portion for extension outside the
5 module and connection to an antenna coil;
a second conductive structure having a second body portion and a second
extended portion, with the second extended portion for extension outside the
module and connection to the antenna coil; and
a third conductive structure completely separated from the first and
10 second conductive structures and for connection to one or more
contact pads inside the module.
11. The lead frame of claim 10, wherein the first conductive structure
includes a recessed region for receiving an integrated-circuit chip.
12. The lead frame of claim 10, wherein the first conductive structure is
between the first and second conductive structures.
13. A method of making a lead frame for a radio-frequency identification
20 module, the method comprising:
forming at least a first and a second recessed region in a layer of metal;
and
forming the layer of metal into at least three contact structures, with at
least one of the structures positioned between the first and second
25 recessed regions.
14. The method of claim 13, wherein forming the first and the second
recessed region comprises etching first and second corresponding portions of the
layer of metal.
15. The method of claim 13, further comprising silver plating at least a
portion of each of the three contact structures.

16. A radio-frequency-identification module comprising:
first and second electrically connected chips, with one of the chips
including an external terminal and at least two capacitors, with
each of the capacitors coupled to the external terminal; and
5 first and second external leads electrically coupled to the first and second
chips, with the external leads for connection to an antenna coil.
17. The module of claim 16, further comprising:
an insulative housing enclosing the first and second electrically
10 connected chips.
18. The module of claim 16, wherein the first chip comprises a wireless-
communications circuit and a memory.
- 15 19. The module of claim 16, wherein the two capacitors define a center-
tapped capacitor.
20. A radio-frequency-identification module comprising:
a lead frame having at least first, second, and third separate contact
20 regions, with the first and second contact regions for connection
to an antenna coil;
an RFID chip fixed relative to the first contact region;
a capacitor chip fixed relative to the second contact region and having at
least first, second, and third terminals;
25 a first electrical connection between the first terminal and the first contact
region;
a second electrical connection between the second terminal and the
second contact region; and
a third electrical connection between the third terminal and the the third
30 contact region.

21. The module of claim 20, wherein the RFID chip and the capacitor chip are attached respectively to the first and second contact regions.
22. The module of claim 20, wherein the capacitor chip includes a center-tapped capacitor.
23. An assembly comprising:
an antenna coil having first and second conductive portions; and
the module of claim 20, wherein the first and second contact regions are coupled respectively to the first and second conductive portions.

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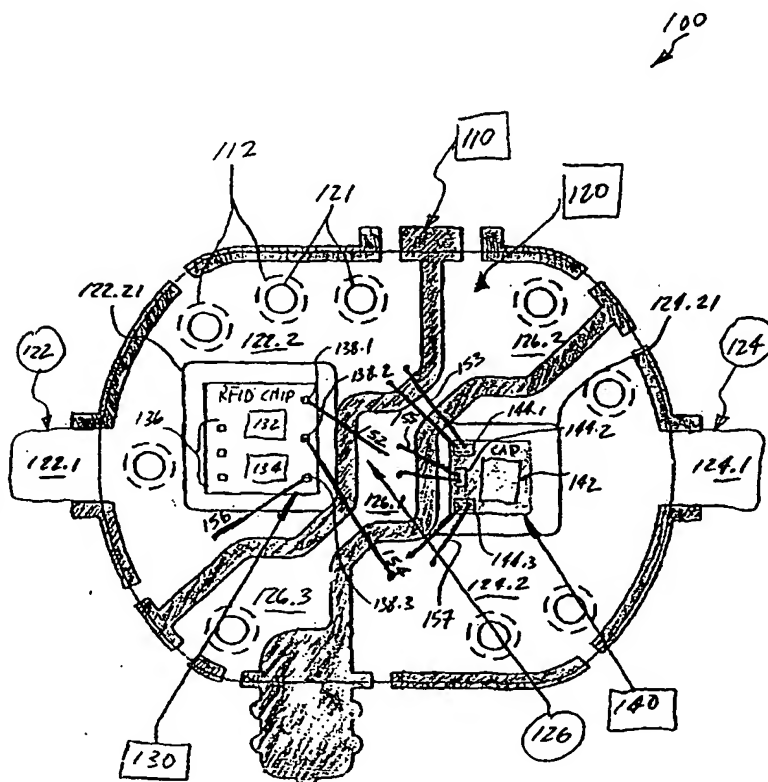
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|------------|---|-----------------------|
| X | WO 00 38110 A (KONINKL PHILIPS ELECTRONICS NV) 29 June 2000 (2000-06-29) page 3, line 10 - line 17 page 4, line 20 -page 6, line 18 page 6, line 33 -page 7, line 9 page 8, line 11 - line 26 | 1-18,20, 21,23 |
| Y | | 19,22 |
| Y | US 4 356 529 A (KOPEL JEFFREY L) 26 October 1982 (1982-10-26) column 3, line 23 - line 38; figures 2,8 | 19,22 |
| A | | 1,16 |
| A | US 6 072 383 A (GALLAGHER III WILLIAM F ET AL) 6 June 2000 (2000-06-06) column 11, line 26 -column 12, line 18; figures 3,4 | 16-19 |
| | -/-- | |



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

| Category * | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|------------|---|-----------------------|
| A | WO 00 36557 A (SLUNSK & YACUTE ; SLUNSKA JUDITA (SK)) 22 June 2000 (2000-06-22) page 4, line 15 -page 5, line 14 | 8-10,16, 20,23 |
| A | WO 97 12263 A (SEITZ MAX GERHARD ; BEER GOTTFRIED (DE); GOTTLIEB ALFRED (DE); SCHW) 3 April 1997 (1997-04-03) page 4, line 13 -page 5, line 35; figures 2,3 | 1-23 |
| A | US 5 420 757 A (DELBEQ JEAN-MARC ET AL) 30 May 1995 (1995-05-30) column 4, line 10 -column 5, line 22 column 6, line 37 -column 7, line 2; figures 1,4,5 | 1-23 |

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

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| Patent document cited in search report | | Publication date | Patent family member(s) | Publication date |
|---|---|---------------------|---|--|
| WO 0038110 | A | 29-06-2000 | WO 0038110 A1 EP 1055193 A1 US 6275158 B1 | 29-06-2000 29-11-2000 14-08-2001 |
| US 4356529 | A | 26-10-1982 | CA 1160357 A1 | 10-01-1984 |
| US 6072383 | A | 06-06-2000 | AU 1112900 A CN 1324545 T EP 1127469 A1 WO 0027137 A1 | 22-05-2000 28-11-2001 29-08-2001 11-05-2000 |
| WO 0036557 | A | 22-06-2000 | WO 0036557 A1 AU 1432199 A | 22-06-2000 03-07-2000 |
| WO 9712263 | A | 03-04-1997 | DE 19536464 A1 WO 9712263 A2 DE 19549343 A1 | 10-04-1997 03-04-1997 03-04-1997 |
| US 5420757 | A | 30-05-1995 | AU 673423 B2 AU 6394394 A CA 2155208 A1 EP 0683923 A1 JP 8506708 T WO 9418700 A1 | 07-11-1996 29-08-1994 18-08-1994 29-11-1995 16-07-1996 18-08-1994 |